

ABSTRACT

The present invention relates to a semiconductor device and, more particularly, has for its object to provide a technique for improving the performance of a semiconductor device having a ground terminal and a plurality of signal terminals
5 arranged around the ground terminal.

To attain the object, the present invention features isolation between a ground terminal (5, 35) connected to a functional block (11) and a ground terminal (6, 36) connected to a functional block (12). Thus, a ground potential applied to one of the
10 functional blocks through the corresponding ground terminal is prevented from varying depending on the magnitude of a current flowing through the other functional block. This improves the performance of each functional block to improve the performance of the semiconductor device.